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L Number	Hits	Search Text	DB	Time stamp
1	390	(715/503,504,509).CCLS.	USPAT; US-PGPUB; EPO; JPO;	2004/08/28 11:54
2	53572	<pre>(creat\$3 generat\$3) near3 (spreadsheet table (spread adj1 sheet))</pre>	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/08/28 12:28
3	12106	(defin\$3 specif\$4) with number with (row column)	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/08/28 11:56
4	46	((715/503,504,509).CCLS.) and ((creat\$3 generat\$3) near3 (spreadsheet table (spread adj1 sheet))) and ((defin\$3 specif\$4) with number with (row column))	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/08/28 12:25
5	233792		IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/08/28 14:53
6	60013	(creat\$3 develop\$4 generat\$3) near3 (spreadsheet table (spread adj1 sheet))	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/08/28 14:45
7	204	((defin\$3 specif\$4) with number with (row column)) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((creat\$3 develop\$4 generat\$3) near3	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 12:29
8	204	column)) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet table (spread adj1 sheet)))) not (((715/503,504,509).CCLS.) and ((creat\$3 generat\$3) near3 (spreadsheet table (spread adj1 sheet))) and ((defin\$3 specif\$4) with	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:39
9	4305	number with (row column))) data adj1 driven	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/08/28 14:39
10	139	(data adj1 driven) with (grid table spreadsheet matrix (spread adj1 sheet))	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/08/28 14:46
11	13	with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((data adj1 driven) with (grid table spreadsheet	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/08/28 14:40
12	96633	<pre>matrix (spread adj1 sheet))) (creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))</pre>	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/08/28 14:45
13	36010	data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet))	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/28 14:47

14	1003	((design\$3 manufactur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic	USPAT; US-PGPUB;	2004/08/28 14:47
		chip (integrated adj1 circuit))) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet)))	EPO; JPO; DERWENT; IBM_TDB	
15	15102	cell with (popup pop-up ((pull drop) adjl down) ad j1 (menu list))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/28 14:50
16	85	<pre>(((design\$3 manufactur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet)))) and (cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list)))</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:51
17	8	((defin\$3 specif\$4) with number with (row column)) and ((((design\$3 manufactur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet)))) and (cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:51
18	547979	(design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adjl circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/28 14:54
19	8	((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet))) and (cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list))) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 15:03
20	13	((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list))) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 14:59

21	5	(((defin\$3 specif\$4) with number with (row	USPAT;	2004/08/28 14:59
21		column)) and ((creat\$3 develop\$4 generat\$3)	US-PGPUB:	2004/00/20 14:59
			EPO; JPO;	
		near3 (spreadsheet matrix grid table (spread		
		adj1 sheet))) and (cell with (popup pop-up	DERWENT;	
		((pull drop) adj1 down) ad j1 (menu list)))	IBM_TDB	
		and ((design\$3 manufactur\$3 model\$3		
		fabrica\$3) with (semiconductor		:
		microcontroller pla pld fpga mcm ic chip		
		(integrated adj1 circuit)))) not (((defin\$3		
		specif\$4) with number with (row column)) and		
		((creat\$3 develop\$4 generat\$3) near3		
		(spreadsheet matrix grid table (spread adj1		
		sheet))) and (data with (defin\$3 configur\$3		
		specif\$4) with (grid table spreadsheet		
		matrix (spread adj1 sheet))) and (cell with		
		(popup pop-up ((pull drop) adj1 down) ad j1		
		(menu list))) and ((design\$3 manufactur\$3		
		model\$3 fabrica\$3) with (semiconductor		
		microcontroller pla pld fpga mcm ic chip		
		(integrated adj1 circuit))))		
22	145	((defin\$3 specif\$4) with number with (row	USPAT;	2004/08/28 15:03
		column)) and ((creat\$3 develop\$4 generat\$3)	US-PGPUB;	
		near3 (spreadsheet matrix grid table (spread	EPO; JPO;	
		adj1 sheet))) and (data with (defin\$3	DERWENT;	
		configur\$3 specif\$4) with (grid table	IBM TDB	
		spreadsheet matrix (spread adj1 sheet))) and	_	
		((design\$3 manufactur\$3 model\$3 fabrica\$3)		
		with (semiconductor microcontroller pla pld		
		fpga mcm ic chip (integrated adj1 circuit)))		
23	85	(data adj1 driven) and (((defin\$3 specif\$4)	USPAT;	2004/08/28 15:03
		with number with (row column)) and ((creat\$3	US-PGPUB;	
		develop\$4 generat\$3) near3 (spreadsheet	EPO; JPO;	
		matrix grid table (spread adj1 sheet))) and	DERWENT;	
		(data with (defin\$3 configur\$3 specif\$4)	IBM TDB	
		with (grid table spreadsheet matrix (spread	_	
		model\$3 fabrica\$3) with (semiconductor		
		microcontroller pla pld fpga mcm ic chip		
		(integrated adjl circuit))))		
23	85	with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet))) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip	US-PGPUB; EPO; JPO; DERWENT;	2004/08/28 15:03

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24	85	<pre>((data adj1 driven) and (((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet))) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))))) not ((((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list))) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit)))) (((defin\$3 specif\$4) with number with (row column)) and ((((design\$3 manufactur\$3 model\$3 fabrica\$3)) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((creat\$3 develop\$4 generat\$3) near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table spreadsheet matrix (spread adj1 sheet)))) and (cell with (popup pop-up ((pull drop) adj1 down) ad j1 (menu list))))) (((design\$3 manufactur\$3 model\$3 fabrica\$3) with (microcontroller pla pld fpga mcm ic chip (integrated adj1 circuit))) and ((data adj1 driven) with (grid table spreadsheet matrix (spread adj1 circuit))) and (spreadsheet matrix (spread adj1</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/28 15:08
25	146	<pre>sheet))))) (((defin\$3 specif\$4) with number with (row column)) and ((creat\$3 develop\$4 generat\$3)</pre>	USPAT; US-PGPUB;	2004/08/28 15:08
		near3 (spreadsheet matrix grid table (spread adj1 sheet))) and (data with (defin\$3 configur\$3 specif\$4) with (grid table	EPO; JPO; DERWENT; IBM TDB	
		spreadsheet matrix (spread adj1 sheet))) and ((design\$3 manufactur\$3 model\$3 fabrica\$3) with (semiconductor microcontroller pla pld fpga mcm ic chip (integrated adj1	1511_155	
		circuit)))) not23		

<u> </u>	CO.	1//2-5:- da: 664) with number with /row	IICDAT -	2004/08/28 1	5.00
26	60	(((defin\$3 specif\$4) with number with (row	USPAT;	2004/00/20 1	
		column)) and ((creat\$3 develop\$4 generat\$3)	US-PGPUB;		
j		near3 (spreadsheet matrix grid table (spread	EPO; JPO;		
		adj1 sheet))) and (data with (defin\$3	DERWENT;		
		configur\$3 specif\$4) with (grid table	IBM_TDB		
		spreadsheet matrix (spread adjl sheet))) and	1		
İ		((design\$3 manufactur\$3 model\$3 fabrica\$3)			
		with (semiconductor microcontroller pla pld			
		fpga mcm ic chip (integrated adj1			
		circuit)))) not (((data adj1 driven) and			
		(((defin\$3 specif\$4) with number with (row	İ		
		column)) and ((creat\$3 develop\$4 generat\$3)			
		near3 (spreadsheet matrix grid table (spread			
		adj1 sheet))) and (data with (defin\$3			
		configur\$3 specif\$4) with (grid table			
		spreadsheet matrix (spread adj1 sheet))) and			
	+	((design\$3 manufactur\$3 model\$3 fabrica\$3)			
		with (semiconductor microcontroller pla pld			
		fpga mcm ic chip (integrated adj1			
		circuit))))) not ((((defin\$3 specif\$4) with			
		number with (row column)) and ((creat\$3			
		develop\$4 generat\$3) near3 (spreadsheet			
		matrix grid table (spread adj1 sheet))) and			
		(cell with (popup pop-up ((pull drop) adj1			
		down) ad j1 (menu list))) and ((design\$3)			
		manufactur\$3 model\$3 fabrica\$3) with			
		(semiconductor microcontroller pla pld fpqa			
		mcm ic chip (integrated adj1 circuit))))			
		(((defin\$3 specif\$4) with number with (row			
		column)) and ((((design\$3 manufactur\$3			
		model\$3 fabrica\$3) with (microcontroller			
		pla pld fpga mcm ic chip (integrated adj1			
		circuit))) and ((creat\$3 develop\$4			
		generat\$3) near3 (spreadsheet matrix grid			
		table (spread adj1 sheet))) and (data with			
		(defin\$3 configur\$3 specif\$4) with (grid			
		table spreadsheet matrix (spread adj1			
		sheet)))) and (cell with (popup pop-up			
		((pull drop) adj1 down) ad j1 (menu list))			
))) (((design\$3 manufactur\$3 model\$3			
		fabrica\$3) with (microcontroller pla pld			
		fpga mcm ic chip (integrated adj1 circuit)))			
		and ((data adj1 driven) with (grid table			
		spreadsheet matrix (spread adj1 sheet))))))			